

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1. (Original) A method for receiving multiple modes of RF signals according to different radio standards having differing channel spacings, including a first channel spacing in accordance with a first standard and a second channel spacing in accordance with a second standard, wherein the first channel spacing and the second channel spacing are not directly related by integer arithmetic, comprising:

providing a reference frequency;

using the reference frequency to generate channel frequencies for the first standard;

frequency-multiplying the reference frequency by an integer number to produce a derived reference frequency; and

generating channel frequencies for the other standard using a dual-modulus synthesizer and the derived reference frequency.

2. (Original) The method of claim 1, wherein the first standard is GSM and the second standard is IS-136.

Claims 3-9 (Canceled).

10. (New) A clock system for generating first clock signals for a portable wireless communication device operating in accordance with a first radio frequency network standard and second clock signals for the portable wireless communication device operating in accordance with a second radio frequency network standard, comprising:

a multiplier circuit operable to multiply a clock signal by an integer multiplier value and provide a multiplied clock signal;

a reference divider configured to receive the multiplied clock signal and selectively divide the multiplied clock signal by a first integer divisor or a second integer divisor, and provide a first channel spacing clock signal corresponding to a first radio frequency network standard or provide a second channel spacing clock signal corresponding to a second radio frequency network standard; and

a digital frequency phase detector coupled to the reference divider and operable to detect the phase of the first channel spacing clock signal or the phase of the second channel spacing clock signal.

11. (New) The clock system of Claim 10, further comprising a voltage controlled oscillator (VCO) configured to receive a steering voltage from said digital frequency phase detector and generate radio frequency signals in a band of the first radio frequency network standard or a band of the second radio frequency network standard.

12. (New) The clock system of Claim 11 wherein the VCO is further configured to operate as a band selector.

13. (New) The clock system of Claim 11, further comprising a divider circuit configured to receive a digitized version of the radio frequency signals in the band of the first radio frequency network standard or a digitized version of the radio frequency signals in the band of the second radio frequency network standard, said divider circuit operable to selectively divide the digitized version of the radio frequency signals in the band of the first radio frequency network standard or the digitized version of the radio frequency signals in the band of the second radio frequency network standard by a first channel integer or a second channel integer.

14. (New) The clock system of Claim 13 wherein the divider circuit is configured to provide a reference signal to which the digital frequency phase detector can lock onto.